



School of Engineering  
Department of Electrical and Computer Engineering

332:223 Principles of Electrical Engineering I Laboratory

Experiment V

## *Sinusoidal Steady-State Analysis*

### 1 Introduction

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- Objectives**
- To demonstrate the properties of simple series R-C, R-L and R-L-C circuits.
  - To demonstrate phasor analysis as a tool for the analysis of circuits containing frequency-dependent impedances.
  - To experimentally explore the effect of Capacitors and Inductors on the phase of voltages and currents in a circuit

#### Overview

This experiment is designed to demonstrate the analysis of R-C, R-L and R-L-C circuits by the use of phasors. A summary of phasors and phasor analysis of sinusoidal steady state is presented in section 2.

The prelab exercises are designed to promote familiarity with the concepts and involve calculations of the values of several elements that will be used in the experiments. The three actual laboratory experiments are designed to verify the concepts by direct measurement of voltages, currents and resistances. The difference of a real and an ideal inductor is made clear with the measurement of the associated d.c. resistance and its incorporation in the calculation of the resistance value to be used experimentally.

## 2 Theory

### 2.1 The sinusoidal source<sup>1</sup>

A sinusoidal voltage or current source (independent or dependent) produces a voltage or current that varies sinusoidally with time. A sine wave (say an alternating periodic voltage  $v(t)$ ) is uniquely described by the equation

$$v(t) = V_m \cos(\omega t + \phi) + V_{avg}$$

As mentioned in lab II, all of the above parameters (*amplitude  $V_m$ , frequency  $\omega$ , phase angle  $\phi$  and DC offset  $V_{avg}$* ) are needed to uniquely define a signal. For this reason each of them is called a *partial descriptor*. If the DC offset  $V_{avg}$  is of no importance, only the ac quantity is dealt with:

$$v(t) = V_m \cos(\omega t + \phi) \quad (1)$$

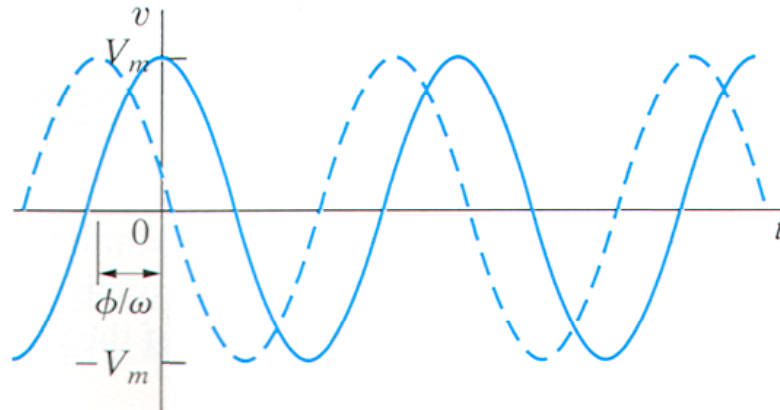


Fig. 1 Sinusoidal voltages with and without phase shift

- Since the cosine function is bounded by  $\pm 1$ , the amplitude is bounded by  $\pm V_m$ .
- The phase angle  $\phi$  describes the phase shift of the *waveform compared with a time origin*. Changing  $\phi$  shifts the sinusoidal function along the time axis, but has no effect on either the amplitude  $V_m$  or the angular frequency  $\omega$ .
- If  $\phi > 0$ , the sinusoidal function shifts to the left; if  $\phi < 0$ , it shifts to the right.
- The *angular frequency*  $\omega$  has units of radians/sec. It is related to the *frequency* of the wave,  $f$  (Hz) by:

$$\omega = 2\pi f$$

<sup>1</sup> A more detailed description can be found in section 9.1 of the text.

## 2.2 Phasor representation of a sinusoid<sup>2</sup>

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Using the Euler identity,  $e^{\pm j\theta} = \cos(\theta) \pm j\sin(\theta)$

Eq. 1 can be written as follows:

$$v(t) = V_m \Re\{e^{j(\omega t + \phi)}\} = V_m \Re\{e^{j\omega t} e^{j\phi}\} = \Re\{(V_m e^{j\phi}) e^{j\omega t}\}$$

The complex number  $V_m e^{j\phi}$  is called the *phasor representation* or the *phasor transform* of  $v(t)$ . Phasors include only two of the partial descriptors e.g. they convey no frequency information but they greatly simplify notation. For example Eq. 1 can be represented in the phasor domain as follows<sup>3</sup>:

$$\mathbf{V} = V_m \angle \phi$$

## 2.3 The passive circuit elements in the Phasor Domain<sup>4</sup>

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If  $i(t)$  is the current flowing through a resistor, inductor, or a capacitor, and

$$i(t) = I_m \cos(\omega t + \theta_i) \text{ then in phasor notation } \mathbf{I} = I_m \angle \theta_i$$

and the voltage in the phasor domain in each case is:

a- Across a resistor:

$$\mathbf{V} = R\mathbf{I} = R I_m \angle \theta_i \quad (2)$$

b- Across an inductor:

$$\mathbf{V} = (j\omega L)\mathbf{I} = \omega L I_m \angle \theta_i + 90^\circ \quad (3)$$

c- Across a capacitor:

$$\mathbf{V} = (1/j\omega C)\mathbf{I} = -(j\omega C)\mathbf{I} = I_m/\omega C \angle \theta_i - 90^\circ \quad (4)$$

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<sup>2</sup> A more detailed description can be found in section 9.3 of the text.

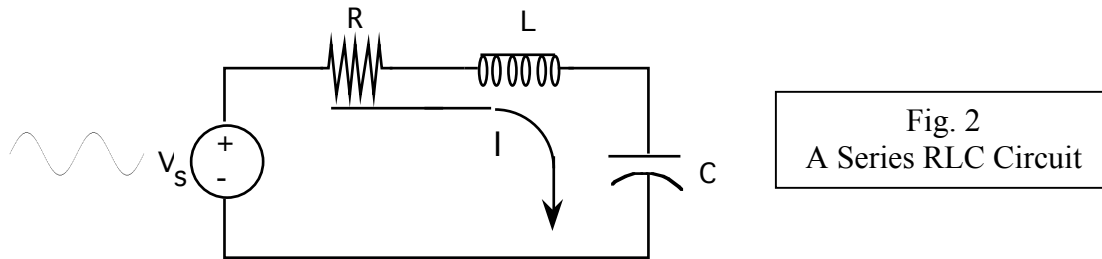
<sup>3</sup> In agreement with the notation of the text, phasors and only phasors will be represented by capital bold symbols throughout this lab.

<sup>4</sup> The subject is treated in more detail in section 9.4 of the text.

## 2.4 Phasor Diagrams<sup>5</sup>

Consider a series RLC circuit with a sinusoidal voltage input  $V_s$  as shown in fig. 2.

$$v_s(t) = V_{sm} \cos(\omega t + \phi)$$

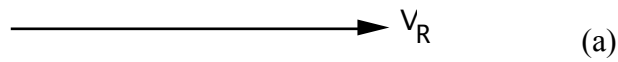


In the phasor domain,

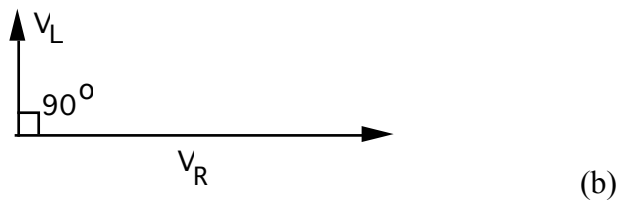
$$\begin{aligned} V_s &= V_R + V_L + V_C = RI + j\omega LI - j\omega CI \\ &= RI_m \angle \theta_i + \omega LI_m \angle \theta_i + 90^\circ + I_m/\omega C \angle \theta_i - 90^\circ \end{aligned}$$

To draw the *phasor diagram* of Fig. 2, for ideal circuit elements, one needs to proceed as follows:

- (1) First a *reference voltage* is chosen, say  $V_R$ ; in other words the angle  $\theta_{VR}$  is set to zero; since there is no phase difference between  $V_R$  and  $I$ , this means  $\theta_i = \theta_{VR} = 0$ .

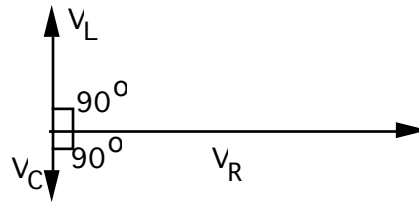


- (2) From the equation above it is clear that  $V_L = \omega LI_m \angle 90^\circ$  will lead  $V_R$  by  $90^\circ$



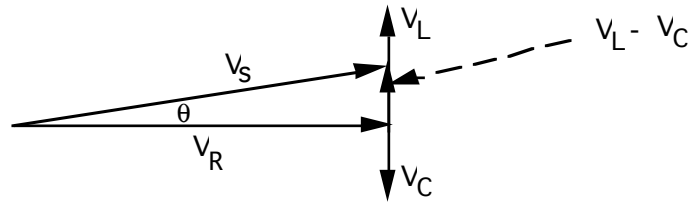
<sup>5</sup> The subject is treated in more detail in section 9.12 of the text.

(3) From the equation above it is clear that  $V_C = I_m/\omega C \angle -90^\circ$  will lag behind  $V_R$  by  $90^\circ$



(c)

(4)  $V_s = V_R + V_L + V_C$



(d)

From inspection of the resulting triangle it follows that:

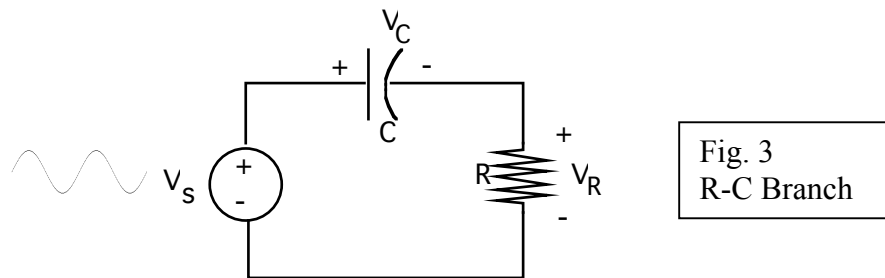
- The magnitude of  $V_s$  is:  $|V_s| = [V_R^2 + (V_L - V_C)^2]^{1/2}$
- The phase angle of  $V_s$  is:  $\theta = \tan^{-1}[(V_L - V_C)/V_R]$

### 3 Prelab Exercises

#### 3.1 R-C Branch

Refer to Fig.3 and compute R and C for  $Z = 2556 \angle -38.5^\circ \Omega$  at  $f = 1000\text{Hz}$ . Note that

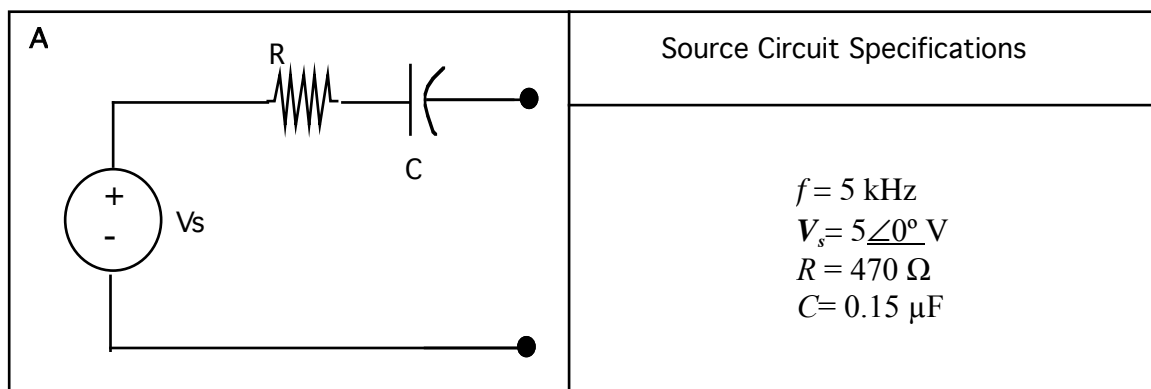
$$Z = R - \frac{j}{\omega C} = \sqrt{R^2 + \frac{1}{\omega^2 C^2}} \angle -\tan^{-1}\left(\frac{1}{\omega CR}\right)$$



#### 3.2 Maximum power A

For the following source circuit, design a load circuit that will extract maximum power from it. Your design should minimize the number of parts and must be within 5% of transferring maximum power to the load.

Calculate the complex power that is delivered to the load.



### 3.3 R-L Branch

Refer to Fig. 4 and compute  $R$  and  $L$  for  $Z = 1181 \angle 32.14^\circ \Omega$  at  $f=1000\text{Hz}$ . Note that

$$Z = R + j\omega L = \sqrt{R^2 + \omega^2 L^2} \angle \tan^{-1}(\omega L / R)$$

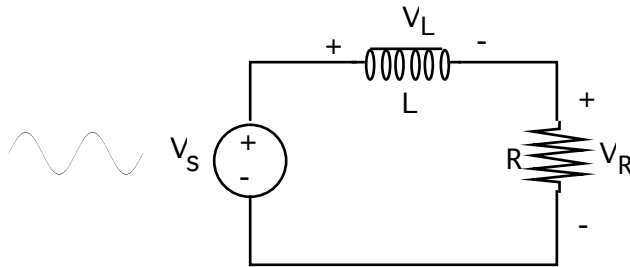


Fig. 4  
R-L Branch

### 3.4 R-C branch maximum power B

For the following source circuit, design a load circuit that will extract maximum power from it. Your design should minimize the number of parts and must be within 5% of transferring maximum power to the load.

Calculate the complex power that is delivered to the load.

<p><b>B</b></p> <p>The diagram shows a circuit with a voltage source <math>V_s</math> (circle with + and -) in series with a resistor <math>R</math> (zigzag line). This series combination is connected to a load consisting of a capacitor <math>C</math> (two parallel lines).</p>	<p>Source Circuit Specifications</p> <p><math>f = 10 \text{ kHz}</math>  <math>V_s = 5 \angle 0^\circ \text{ V}</math>  <math>R = 10 \text{ K}\Omega</math>  <math>C = 0.015 \mu\text{F}</math></p>
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### 3.5 R-L-C branch

Refer to Fig. 5 and compute R and C for  $Z = 1388 \angle -43.9^\circ \Omega$  at  $f = 1000\text{Hz}$ , given  $L = 100\text{mH}$ . Note that

$$Z = R + j\left(\omega L - \frac{1}{\omega C}\right) = \sqrt{R^2 + \left(\omega L - \frac{1}{\omega C}\right)^2} \angle \tan^{-1}\left(\frac{\omega L - \frac{1}{\omega C}}{R}\right)$$

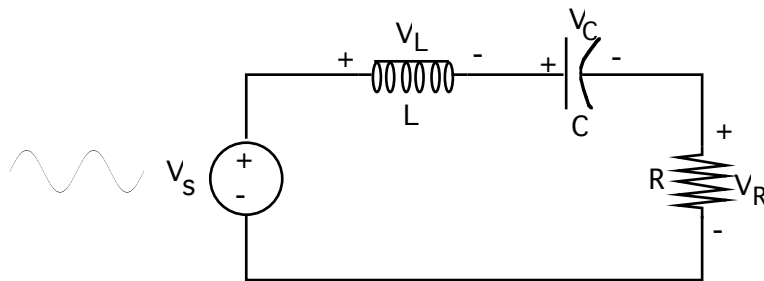


Fig. 5  
R-L-C Branch

## 4 Experiments

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### Suggested Equipment:

Tektronix FG 501A 2MHz Function Generator<sup>6</sup>  
 Tektronix DC 504A Counter-Timer  
 Protek B-845 Digital Multimeter  
 Agilent 54622A oscilloscope  
 LS-400A Inductance Substitute Box  
 1  $\mu$ F Capacitor  
 100  $K\Omega$  resistor  
 Various circuit elements to be determined by students

### 4.1 R-C branch

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- a. With the values of R and C as computed in pre-lab exercise 3.1, build the circuit of Fig.3.
- b. Connect the DVM (switched to AC) across the resistor and adjust the oscillator<sup>7</sup> output<sup>8</sup> until  $V_R$  has the proper value that corresponds to a current of 1mA flowing through the circuit.
- c. Measure  $V_s$ ,  $V_R$ , and  $V_C$ .
- d. Display the time domain signals  $v_s(t)$  and  $v_R(t)$  corresponding to  $V_s$  and  $V_R$  on the oscilloscope, and download them to your floppy disk.
- e. Measure the phase. The oscilloscope can automatically calculate the phase difference angle  $\theta$  between the phasors  $V_s$  and  $V_R$  in the following way:
  - press the *Quick Meas* button
  - press the 2<sup>nd</sup> soft key to select phase
  - press the 3<sup>rd</sup> soft key to measure the phase difference between the two signals

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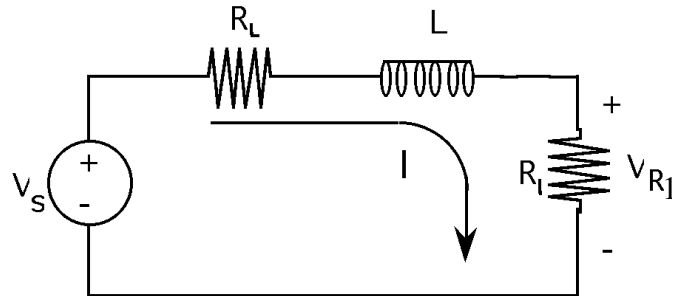
<sup>6</sup> Throughout this experiment, large resistance values will be used; thus, the  $50\Omega$  internal impedance of the function generator can be neglected.

<sup>7</sup> "Oscillator" is routinely used for any source of ac voltage (here the Function Generator).

<sup>8</sup> The oscillator output and the oscilloscope input have a common ground connection, consequently a component voltage cannot be displayed properly on the scope unless that component is also connected to the common ground. Make sure that you build your circuits exactly as shown in the figures.

## 4.2 R-L Branch

- Select an inductor which has the inductance value  $L$  computed in pre-lab exercise 3.3.
- Measure the d.c. resistance  $R_L$  of the inductor using the digital ohmmeter.
- In pre-lab exercise 3.3, a value for the resistance  $R$  of Fig.4 was computed. This computation, however, assumed that  $L$  was a *pure inductance*. It is now seen that a real inductor possesses resistance ( $R_L$ ) *in addition* to its inductance ( $L$ ). In other words the actual circuit looks like



Since  $R$  must be the total resistance in the circuit (as per 3.3 calculations) the resistance of the inductor has to be taken into account. Let then

$$R_I = R - R_L$$

where  $R_L$  is the d.c. resistance of the inductor.

Select a resistor whose resistance value equals  $R_I$ . In other words, the resistance value of the resistor is intentionally decreased in order to take the d.c. resistance of the inductor into account.

- With the values of  $R_I$  and  $L$  as described above, build the series circuit of Fig.4. Set up the circuit exactly as shown in Fig. 4 in order to avoid problems with ground.
- Adjust the oscillator until the output  $V_{R1}$  across the resistor  $R_1$  has the proper value that corresponds to a 1mA current flowing through the circuit.
- Measure the magnitudes of the voltages corresponding to  $V_s$ ,  $V_{R1}$ , and  $V_{L+R_L}$  using the DVM (switched to AC). Note that the physical inductor is a series combination of  $R_L$  and  $L$ . One cannot physically measure the inductance voltage alone. Only the voltage across the inductor which has both the resistance  $R_L$  and inductance  $L$  can be measured.
- Display the time domain signals  $v_s(t)$  and  $v_{R1}(t)$  corresponding to  $V_s$  and  $V_{R1}$  on the oscilloscope, and download them to your floppy disk.

- h. Measure the phase angle  $\theta$  between  $V_s$  and  $V_{RI}$  using the method explained in Section 4.1.e.

### 4.3 R-L-C Branch

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- a. Measure the d.c. resistance  $R_L$  of the inductor whose inductance value is  $L = 100\text{mH}$ .
- b. Build the R-L-C series circuit of Fig. 5 by using the R and C values as computed in pre-lab exercise 3.5. Make sure that the value of resistance R is decreased to  $R_I = R - R_L$  by the d.c. resistance of the inductor as in 4.2.c.
- c. Follow the steps in Section 4.1 and measure  $V_s$ ,  $V_{RI}$ ,  $V_{L+RL}$ ,  $V_C$ , and the phase angle  $\theta$  between  $V_s$  and  $V_{RI}$ ; download the waveforms  $v_s(t)$  and  $v_{RI}(t)$  corresponding to  $V_s$  and  $V_{RI}$  from the oscilloscope.
- d. At resonance the reactance X is zero and the impedance is purely resistive. Since the imaginary portion of the impedance is zero the phase angle is equal to zero.  
Measure the resonant frequency for the circuit of Fig. 5. There are a number of ways of doing so. One way is to tune the frequency of the oscillator until  $V_s$  and  $V_{RI}$  are in phase, (i.e. the phase angle between them is zero) by watching  $v_s(t)$  and  $v_{RI}(t)$ .
- e. For all frequencies above resonance, phase angles have the same sign and for frequencies below the resonant point they have the opposite sign. Verify that this is true over a frequency range of two orders of magnitude extending on both sides of the resonant frequency.

## 5 Report

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- 5.1 For the R-C branch shown in Fig.3, do the following:
- Tabulate  $V_s$ ,  $V_R$ ,  $V_C$ , and  $\theta$  measured in the experiment.
  - Submit a copy of the waveforms  $v_s(t)$  and  $v_R(t)$  of  $V_s$  and  $V_R$
  - Prepare a phasor diagram to scale that represents in phasor domain
 
$$\mathbf{V}_s = \mathbf{V}_R + \mathbf{V}_C.$$
  - Calculate the phase angle  $\theta$  in three different ways:
 
$$\theta = -\tan^{-1}(V_C/V_R) = -\sin^{-1}(V_C/V_s) = -\cos^{-1}(V_R/V_s)$$
 Compare  $\theta$  calculated with  $\theta$  measured.
  - Calculate the average power<sup>9</sup>,  $P$ , in two different ways:
 
$$P = V_R^2/R = V_s I \cos\theta$$
 where  $\theta$  is the measured phase angle.
  - Does  $V_R$  lead or lag  $V_s$ ?
  - For the circuit of Fig. A in pre-lab exercise 3.2, what is the load that consumes the maximum power and what is that value of maximum power?
- 5.2 For the R-L branch shown in Fig.4, do the following:
- Tabulate  $V_s$ ,  $V_{R1}$ ,  $V_{L+RL}$ , and  $\theta$  measured.
  - Submit a copy of the waveforms  $v_s(t)$  and  $v_{R1}(t)$  of  $V_{R1}$  and  $V_s$ .
  - Prepare a phasor diagram to scale. Take the effect of the d.c. resistance of the inductor into account.
  - From the phasor diagram, determine the phase angle  $\theta$  in three different ways as in item 5.1.d. Compare with the measured one.
  - Calculate the average power,  $P$ , in two different ways as in 5.1.e.
  - Does  $V_R$  lead or lag  $V_s$ ?
  - For the circuit of Fig. B in pre-lab exercise 3.4, what is the load that consumes the maximum power and what is that value of maximum power.
- 5.3 For the R-L-C branch in Fig. 5, repeat 5.2 (a-f) for the impedance  $Z$  used in performing the experiment. Do not forget to take into account the d.c. resistance  $R_L$  of the inductor  $L$  in all calculations.
- 5.4 With  $v_s(t) = 10\sin(2\pi ft)$  Volts where  $f=1\text{KHz}$ , use PSpice to plot  $v_s(t)$  and  $v_R(t)$  for the three circuits in figures 3, 4, and 5. Show 2 to 4 periods of the waveforms in your plot. (Hint: Use the TRAN. command.)

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<sup>9</sup> For detailed discussion on average power see section 10.2 of the text.

- 5.5 With  $v_s(t) = 10\sin(2\pi ft)$  Volts where  $f=1\text{KHz}$ , use PSpice to determine the real part, imaginary part, magnitude, and phase of  $V_R$  (or  $V_{R1}$  as the case may be) for the circuits in figures 3, 4, and 5. ( Hint: Use the AC. command.)
- 5.6 Prepare a summary.